



0360

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: R. Daniel McGrath et al. PATENT APPLICATION
Serial No.: 09/872,209 Group Art Unit:
Filed: June 1, 2001 Examiner:
For: DUAL-MODE CMOS INTEGRATED IMAGER

Information Disclosure Statement

Hon. Assistant Commissioner
for Patents
Washington, D.C. 20231

Sir:

The following information is submitted in compliance with Applicants' duty of disclosure under 37 CFR § 1.56. A copy of each reference is enclosed.

U.S. Patents

<u>Pat. No.</u>	<u>Patentee</u>	<u>Grant Date</u>
5,841,126	Fossum et al.	11/24/98
5,615,358	Vogley	03/25/97
5,587,961	Wright et al.	12/24/96
5,495,594	MacKenna et al.	02/27/96
5,394,541	Chesley et al.	02/28/95

CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Asst. Commissioner for Patents, Washington, D.C. 20231

Signed: Sally Azevedo
Typed Name: Sally Azevedo
Date: August 14, 2001

Respectfully submitted,

Thomas Schneck
Reg. No. 24,518

P.O. Box 2-E
San Jose, CA 95109-0005
(408) 297-9733